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By: [Signature]

Date: May 27, 2003

Response # 14 6-9-03 J. Carter

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Thomas Peter Haneder et al.

Applic. No. : 09/801,209

Filed : March 7, 2001

Title : Ferroelectric Transistor, Use Thereof In A Memory Cell Configuration And Method Of Producing The Ferroelectric Transistor

Examiner : Thao X. Le

Group Art Unit : 2814

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S i r :

Responsive to the final Office action dated February 25, 2003

kindly consider the following remarks: